

Six-Channel Digital Isolators

1. General Description

The SiS776x devices are high-performance, six-channel digital isolators with 5000- V_{RMS} (W package), 3000- V_{RMS} (N Package) and 2500 (B package) isolation ratings. The SiS776x family of devices provides high-electromagnetic immunity and low emissions at low-power consumption, while isolating CMOS or LVCMOS digital I/Os. Each isolation channel has a logic-input and logic-output buffer separated by a double capacitive silicon dioxide (SiO_2) insulation barrier.

The SiS776x family offers all possible unidirectional channel configurations to accommodate any 6-channel design digital I/O applications, especial for the multiple SPI devices isolation.

The SiS7760 features six channels transferring digital signals in one direction; The SiS7761 device has five forward and one reverse-direction channels; The SiS7762 device offers four forward and two reverse-direction channels; The SiS7763 provides further design flexibility with three channels in each direction. All devices of this family feature default outputs. When the input is either not powered or is open-circuit, the default output is low for devices with suffix L and high for devices with suffix H, see the Ordering Information for suffixes associated with each option.

2. Application

- Industrial Automation
- Motor Control
- Isolated Power Supplies
- Solar Inverters
- Isolated ADC, DAC etc.

3. Feature

- Data rate: DC to 150Mbps
- Default output High (SiS776xH) and Low (SiS776xL) Options
- Power supply voltage: 2.5V to 5.5V
- 2.5V to 5.5V level translation
- CMTI :100kV/us
- Low propagation delay: 11 ns typical at 5 V
- Operation temperature: $-40^{\circ}C$ to $+125^{\circ}C$
- Safety certifications:
- HBM 8KV ESD Rating
- Three package options
 - Narrow body SSOP16 (B) package
 - Narrow-body SOP16 (N) package
 - Wide-body SOW16 (W) package
- 2500 V_{RMS} isolation for SSOP-16 package
- 3750 V_{RMS} isolation for SOP package
- 5000 V_{RMS} isolation for SOW16 package

4. Device Information

Devices	Package	Body Size (NOM)
SiS7760 SiS7761 SiS7762 SiS7763	(B) SSOP16	4.90mm × 3.90mm
	(N) SOP16	9.90mm × 3.90mm
	(W) SOW16	10.30mm × 7.50mm

5. Absolute Maximum Ratings

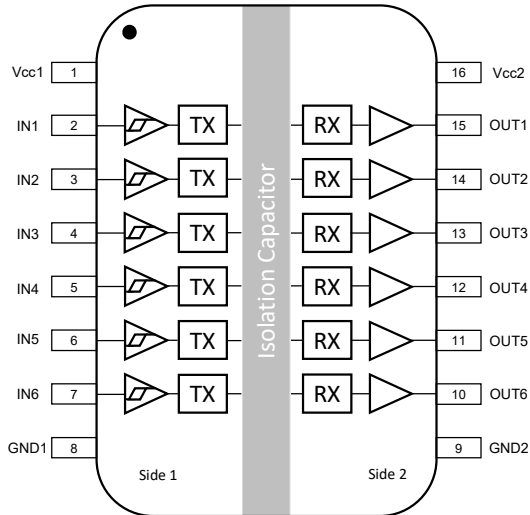
Parameter	Min.	Max.	Unit
V _{CC1} , V _{CC2} Supply Voltage	-0.5	+6.0	V
V _{IN1} , V _{IN2} Input Voltage	-0.5	V _{CC1} + 0.5	
OUT1, OUT2 Output Voltage	-0.5	V _{CC2} + 0.5	
T _{J(MAX)} Maximum junction temperature	-40	150	$^{\circ}C$
T _{stg} Storage temperature	-40	150	$^{\circ}C$

6. Ordering Information

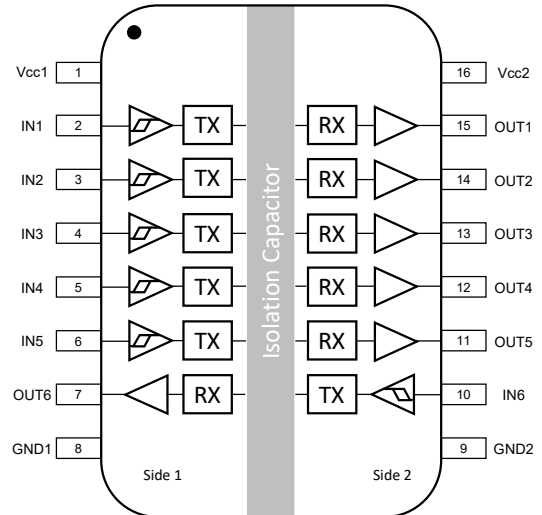
Part Number	Marking	Input Channel	Output Channel	Default Output	Isolation Rating(kV _{RMS})	Package	Packing Form
SiS7760HB	iS7760HB	6	0	High	2.5	SSOP16 (B)	2500 pieces per reel
SiS7761HB	iS7761HB	5	1	High			
SiS7762HB	iS7762HB	4	2	High			
SiS7763HB	iS7763HB	3	3	High			
SiS7760LB	iS7760LB	6	0	Low			
SiS7761LB	iS7761LB	5	1	Low			
SiS7762LB	iS7762LB	4	2	Low			
SiS7763LB	iS7763LB	3	3	Low			
SiS7760HN	iS7760HN	6	0	High	3.75	SOP16 (N)	2500 pieces per reel
SiS7761HN	iS7761HN	5	1	High			
SiS7762HN	iS7762HN	4	2	High			
SiS7763HN	iS7763HN	3	3	High			
SiS7760LN	iS7760LN	6	0	Low			
SiS7761LN	iS7761LN	5	1	Low			
SiS7762LN	iS7762LN	4	2	Low			
SiS7763LN	iS7763LN	3	3	Low			
SiS7760HW	iS7760HW	6	0	High	5.0	SOW16 (W)	1000 pieces per reel
SiS7761HW	iS7761HW	5	1	High			
SiS7762HW	iS7762HW	4	2	High			
SiS7763HW	iS7763HW	3	3	High			
SiS7760LW	iS7760LW	6	0	Low			
SiS7761LW	iS7761LW	5	1	Low			
SiS7762LW	iS7762LW	4	2	Low			
SiS7763LW	iS7763LW	3	3	Low			

7. Pin Configuration and Functions

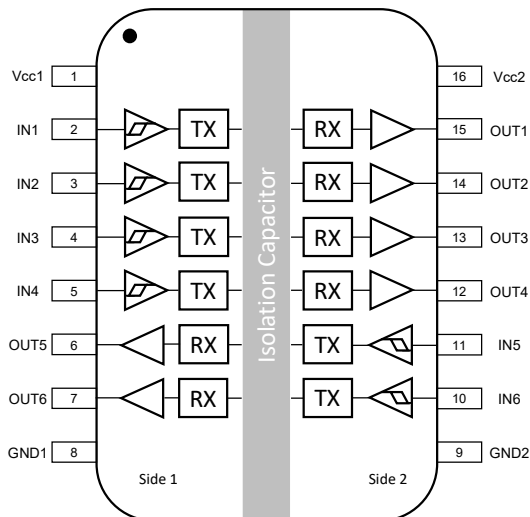
SiS7760 SSOP16 / SOIC-16 Top View



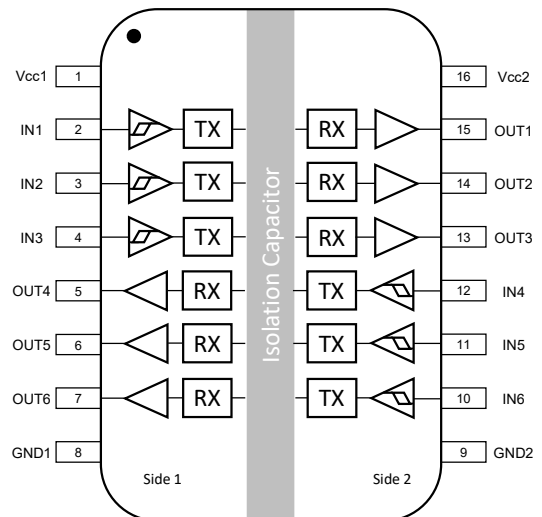
SiS7761 SSOP16 / SOIC-16 Top View



SiS7762 SSOP16 / SOIC-16 Top View



SiS7763 SSOP16 / SOIC-16 Top View



Pin Functions – SiS7760/SiS7761/SiS7762/SiS7763					
SiS7760	SiS7761	SiS7762	SiS7763	Name	Description
1	1	1	1	Vcc1	Power supply for Side 1
2	2	2	2	IN1	Input, Channel 1
3	3	3	3	IN2	Input, Channel 2
4	4	4	4	IN3	Input, Channel 3
5	5	5	12	IN4	Input, Channel 4
6	6	11	11	IN5	Input, Channel 5
7	10	10	10	IN6	Input, Channel 6
8	8	8	8	GND1	Ground, Side 1
9	9	9	9	GND2	Ground, Side 2
10	7	7	7	OUT6	Output, Channel 6
11	11	6	6	OUT5	Output, Channel 5
12	12	12	5	OUT4	Output, Channel 4
13	13	13	13	OUT3	Output, Channel 3
14	14	14	14	OUT2	Output, Channel 2
15	15	15	15	OUT1	Output, Channel 1
16	16	16	16	Vcc2	Power supply for Side 2.

8. Electrical Characteristics---5V Supply

V_{cc1}=5V±10%, V_{cc2}=5V±10%, T_A=25°C (over recommended operating conditions, unless otherwise noted)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{OH} High-level output voltage	I _{OH} = -4 mA	V _{cco} - 0.4	V _{cco} - 0.2		V
V _{OL} Low-level output voltage	I _{OL} = 4 mA		0.2	0.4	V
V _{IH} Logic input high level threshold	V _{cc1} =V _{cc2} =5.0V	2.0			V
V _{IL} Logic input low level threshold	V _{cc1} =V _{cc2} =5.0V			0.8	V
I _{IN} Input current	0 ≤ V _{IN1} , V _{IN2} , V _{IN3} , V _{IN4} , V _{IN5} , V _{IN6} ≤ 5V	-20		20	uA
SiS7760 Supply Current					
Supply current -- DC signal					
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =5.5V V _{IN} =5.5V (SiS7760H), V _{IN} =GND (SiS7760L)		2.6 4.9	5.4 12	mA
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =5.5V V _{IN} =GND (SiS7760H), V _{IN} =5.5V (SiS7760L)		13 5.4	21 12	mA
Supply current – AC signal					
1Mbps					
I _{cc1} I _{cc2}	500kHz logic Frequency		5.6 7.8	12 13	mA
10Mbps					
I _{cc1} I _{cc2}	5MHz logic Frequency		7.9 7.5	12.5 16	mA
150Mbps					
I _{cc1} I _{cc2}	75MHz logic Frequency		9.3 35	20 115	mA
SiS7761 Supply Current					
Supply current -- DC signal					
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =5.5V V _{IN} =5.5V (SiS7761H), V _{IN} =GND (SiS7761L)		3.0 5.5	7.0 14	mA
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =5.5V V _{IN} =GND (SiS7761H), V _{IN} =5.5V (SiS7761L)		11 7.6	19 18	mA
Supply current – AC signal					
1Mbps					
I _{cc1} I _{cc2}	500kHz logic Frequency		7.3 7.0	12 16	mA
10Mbps					

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I _{cc1} I _{cc2}	5MHz logic Frequency		8.0 10	14 18	mA
150Mbps					
I _{cc1} I _{cc2}	75MHz logic Frequency		14 32	36 100	mA
SiS7762 Supply Current					
Supply current -- DC signal					
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =5.5V V _{IN} =5.5V (SiS7762H), V _{IN} =GND (SiS7762L)		3.8 5.0	8.5 12	mA
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =5.5V V _{IN} =GND (SiS7762H), V _{IN} =5.5V (SiS7762L)		11 8.7	17.5 19	mA
Supply current – AC signal					
1Mbps					
I _{cc1} I _{cc2}	500kHz logic Frequency		7.4 7.2	12 16	mA
10Mbps					
I _{cc1} I _{cc2}	5MHz logic Frequency		8.7 9.6	15 17	mA
150Mbps					
I _{cc1} I _{cc2}	75MHz logic Frequency		19 28	51 82	mA
SiS7763 Supply Current					
Supply current -- DC signal					
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =5.5V V _{IN} =5.5V (SiS7763H), V _{IN} =GND (SiS7763L)		4.6 4.6	8.5 8.5	mA
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =5.5V V _{IN} =GND (SiS7763H), V _{IN} =5.5V (SiS7763L)		10 10	17 17	mA
Supply current – AC signal					
1Mbps					
I _{cc1} I _{cc2}	500kHz logic Frequency		7.6 7.6	13 13	mA
10Mbps					
I _{cc1} I _{cc2}	5MHz logic Frequency		9.4 9.4	16 16	mA

150Mbps				
Icc1	75MHz logic Frequency	22	63	mA
Icc2		22	63	

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
PW _{min}	C _L =15pF	6.67			ns
PWD Pulse width distortion t _{PHL} - t _{PLH}	C _L =15pF		2	6	ns
DR Data Rate	C _L =15pF	0		150	Mbps
t _{PLH} , t _{PHL} Propagation Delay Time	C _L =15pF		11	24	ns
tsk (CD) Channel-to-channel Output Skew Time ¹	Same direction channels			3.6	ns
tsk (OD) Channel-to-channel Output Skew Time ¹	Reverse direction channels			3.6	ns
t _r Output signal rise time	C _L =15pF		3	5	ns
t _f Output signal fall time	C _L =15pF		3	5	ns
CMTI Common-mode Transient Immunity	C _L =15pF	100			kV/us

① VccI = Input-side Vcc; Vcco = Output-side Vcc.

9. Electrical Characteristics---3.3V Supply

Vcc1=3.3V±10%, Vcc2=3.3V±10%, T_A=25°C (over recommended operating conditions, unless otherwise noted)

Parameter		Test Conditions	Mni.	Typ.	Max.	Unit
Side 1 (ONLY)						
V _{OH}	High-level output voltage	I _{OH} =-4 mA	V _{cco} -0.4	V _{cco} -0.2		V
V _{OL}	Low-level output voltage	I _{OL} = 4 mA		0.2	0.4	V
V _{IH}	Logic input high level threshold	V _{cc} 1=V _{cc} 2=3.3V	2.0			V
V _{IL}	Logic input low level threshold	V _{cc} 1=V _{cc} 2=3.3V		0.8		V
I _{IN}	Input current	0≤V _{IN1} , V _{IN2} ≤5V	-20	20		uA
SiS7760 Supply Current						
Supply current -- DC signal						
I _{cc} 1		V _{cc} 1=V _{cc} 2=3.3V V _{IN} =3.3V (SiS7760H), V _{IN} =GND (SiS7760L)		2.5	5.2	mA
I _{cc} 2				4.8	12	
I _{cc} 1		V _{cc} 1=V _{cc} 2=3.3V V _{IN} =GND (SiS7760H), V _{IN} =3.3V (SiS7760L)		13	20	mA
I _{cc} 2				7.8	12	
Supply current – AC signal						
1Mbps						
I _{cc} 1		500kHz logic Frequency		7.7	11.8	mA
I _{cc} 2				7.0	12	
10Mbps						
I _{cc} 1		5MHz logic Frequency		7.7	12	mA
I _{cc} 2				7.9	14	
150Mbps						
I _{cc} 1		75MHz logic Frequency		9.2	17	mA
I _{cc} 2				26	76	
SiS7761 Supply Current						
Supply current -- DC signal						
I _{cc} 1		V _{cc} 1=V _{cc} 2=3.3V V _{IN} =3.3V (SiS7761H), V _{IN} =GND (SiS7761L)		3.0	7.0	mA
I _{cc} 2				5.5	13.5	
I _{cc} 1		V _{cc} 1=V _{cc} 2=3.3V V _{IN} =GND (SiS7761H), V _{IN} =3.3V (SiS7761L)		11	18.5	mA
I _{cc} 2				7.5	18	
Supply current – AC signal						
1Mbps						

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I _{cc1} I _{cc2}	500kHz logic Frequency		7.1 6.7	12 15	mA
10Mbps					
I _{cc1} I _{cc2}	5MHz logic Frequency		7.6 8.9	13 16	mA
150Mbps					
I _{cc1} I _{cc2}	75MHz logic Frequency		15 25	27 68	mA
SiS7762 Supply Current					
Supply current -- DC signal					
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =3.3V V _{IN} =3.3V (SiS7762H), V _{IN} =GND (SiS7762L)		3.7 4.9	8.3 12	mA
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =3.3V V _{IN} =GND (SiS7762H), V _{IN} =3.3V (SiS7762L)		11 8.6	17 19	
Supply current – AC signal					
1Mbps					
I _{cc1} I _{cc2}	500kHz logic Frequency		7.2 6.9	12 14	mA
10Mbps					
I _{cc1} I _{cc2}	5MHz logic Frequency		8.1 8.7	14 15	mA
150Mbps					
I _{cc1} I _{cc2}	75MHz logic Frequency		17 22	37 52	mA
SiS7763 Supply Current					
Supply current -- DC signal					
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =3.3V V _{IN} =3.3V (SiS7763H), V _{IN} =GND (SiS7763L)		4.5 4.5	8.3 8.3	mA
I _{cc1} I _{cc2}	V _{cc1} =V _{cc2} =3.3V V _{IN} =GND (SiS7763H), V _{IN} =3.3V (SiS7763L)		10 10	16 16	
Supply current – AC signal					
1Mbps					
I _{cc1} I _{cc2}	500kHz logic Frequency		7.4 7.4	12 12	mA

10Mbps					
lcc1	5MHz logic Frequency	8.7		14	mA
lcc2		8.7		14	
150Mbps					
lcc1	75MHz logic Frequency	19		46	mA
lcc2		19		46	

Parameter		Test Conditions	Min.	Typ.	Max.	Unit
PWmin		$C_L=15\text{pF}$	6.67			ns
PWD	Pulse width distortion ($ t_{PHL}-t_{PLH} $)	$C_L=15\text{pF}$		2	6	ns
DR	Data Rate	$C_L=15\text{pF}$	0		150	Mbps
t_{PLH}, t_{PHL}	Propagation Delay Time	$C_L=15\text{pF}$		12	26	ns
$tsk_{(CD)}$	Channel-to-channel Output Skew Time ¹	Same direction channels			3.6	ns
$tsk_{(OD)}$	Channel-to-channel Output Skew Time ¹	Reverse direction channels			3.6	ns
t_r	Output signal rise time	$C_L=15\text{pF}$		3	5	ns
t_f	Output signal fall time	$C_L=15\text{pF}$		3	5	ns
CMTI	Common-mode Transient Immunity	$C_L=15\text{pF}$	100			kV/us

10. Electrical Characteristics---2.5V Supply

Vcc1=2.5V±10%, Vcc2=2.5V±10%, T_A=25°C (over recommended operating conditions, unless otherwise noted)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Side 1 (ONLY)					
V _{OH} High-level output voltage	I _{OH} = -4 mA	V _{CCO} -0.4	V _{CC} -0.2		V
V _{OL} Low-level output voltage	I _{OL} = 4 mA		0.2	0.4	V
V _{IH} Logic input high level threshold	V _{CC1} =V _{CC2} =2.5V	2.0			V
V _{IL} Logic input low level threshold	V _{CC1} =V _{CC2} =2.5V			0.8	V
I _{IN} Input current	0 ≤ V _{IN1} , V _{IN2} ≤ 5V	-20		20	uA
SiS7760 Supply Current					
Supply current -- DC signal					
I _{CC1} I _{CC2}	V _{CC1} =V _{CC2} =2.5V V _{IN} =3.6V (SiS7760H), V _{IN} =GND (SiS7760L)		2.5 4.8	5.1 12	mA
I _{CC1} I _{CC2}	V _{CC1} =V _{CC2} =2.5V V _{IN} =GND (SiS7760H), V _{IN} =2.5V (SiS7760L)		12 5.3	20 12	mA
Supply current – AC signal					
1Mbps					
I _{CC1} I _{CC2}	500kHz logic Frequency		7.6 5.2	11.7 12.0	mA
10Mbps					
I _{CC1} I _{CC2}	5MHz logic Frequency		7.7 7.1	12.0 13.0	mA
150Mbps					
I _{CC1} I _{CC2}	75MHz logic Frequency		9.5 21	15.0 57.0	mA
SiS7761 Supply Current					
Supply current -- DC signal					
I _{CC1} I _{CC2}	V _{CC1} =V _{CC2} =2.5V V _{IN} =2.5V (SiS7761H), V _{IN} =GND (SiS7761L)		2.9 5.4	7.0 13.5	mA
I _{CC1} I _{CC2}	V _{CC1} =V _{CC2} =2.5V V _{IN} =GND (SiS7761H), V _{IN} =2.5V (SiS7761L)		11.0 7.5	18.5 18.0	mA
Supply current – AC signal					
1Mbps					
I _{CC1} I _{CC2}	500kHz logic Frequency		7.0 6.6	12 14	mA

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
10Mbps					
Icc1 Icc2	5MHz logic Frequency		7.5 8.3	12.5 15	mA
150Mbps					
Icc1 Icc2	75MHz logic Frequency		14 20	22 53	mA
SiS7762 Supply Current					
Supply current -- DC signal					
Icc1 Icc2	Vcc1=Vcc2=2.5V VIN=2.5V (SiS7762H), VIN=GND (SiS7762L)		3.7 4.9	8.2 12	mA
Icc1 Icc2	Vcc1 = Vcc2 =2.5V VIN=GND (SiS7762H), VIN=2.5V (SiS7762L)		11 8.6	17 19	mA
Supply current – AC signal					
1Mbps					
Icc1 Icc2	500kHz logic Frequency		7.1 6.8	12 13	mA
10Mbps					
Icc1 Icc2	5MHz logic Frequency		7.9 8.2	13 14	mA
150Mbps					
Icc1 Icc2	75MHz logic Frequency		14 18	30 45	mA
SiS7763 Supply Current					
Supply current -- DC signal					
Icc1 Icc2	Vcc1=Vcc2=2.5V VIN=2.5V (SiS7763H), VIN=GND (SiS7763L)		4.5 4.5	8.2 8.2	mA
Icc1 Icc2	Vcc1 = Vcc2 =2.5V VIN=GND (SiS7762H), VIN=2.5V (SiS7762L)		9.9 9.9	16 16	mA
Supply current – AC signal					
1Mbps					
Icc1 Icc2	500kHz logic Frequency		7.3 7.3	12 12	mA
10Mbps					

lcc1	5MHz logic Frequency	8.3	13.5	mA
lcc2		8.3	13.5	
150Mbps				
lcc1	75MHz logic Frequency	16	37	mA
lcc2		16	37	

Parameter		Test Conditions	Min.	Typ.	Max.	Unit
PWmin		C _L =15pF	6.67			ns
PWD	Pulse width distortion (t _{PHL} - t _{PLH})	C _L =15pF			6	ns
DR	Data Rate	C _L =15pF	0		150	Mbps
t _{PLH} , t _{PHL}	Propagation Delay Time	C _L =15pF		13	28	ns
tsk (CD)	Channel-to-channel Output Skew Time ¹	Same direction channels			3.6	ns
tsk (OD)	Channel-to-channel Output Skew Time ¹	Reverse direction channels			3.6	ns
t _r	Output signal rise time	C _L =15pF		3	5	ns
t _f	Output signal fall time	C _L =15pF		3	5	ns
SiS772xH Output Voltage		Vcc1=Vcc2=5.0V, Input is open	2.25	2.5	2.75	V
SiS772xL Output Voltage		Vcc1=Vcc2=5.0V, Input is open	-0.25	0	0.25	V
CMTI	Common-mode Transient Immunity	C _L =15pF	100			kV/us

11. Recommended Operating Conditions

Parameter		Min.	Max.	Unit
V _{CC1} , V _{CC2}	Supply voltage	2.5	5.5	V
T _A	Ambient temperature	-45	125	°C

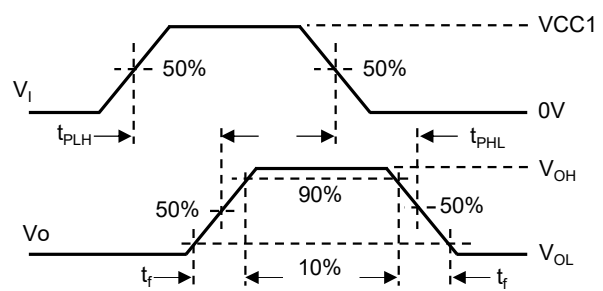
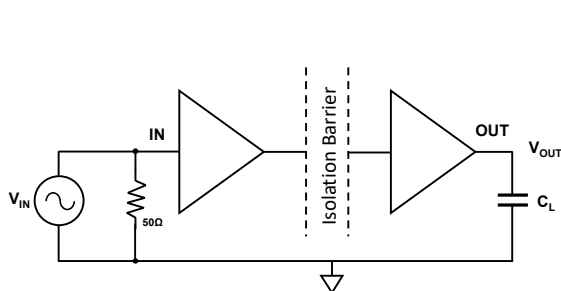
12. Thermal Information

Thermal Metric	B (SSOP16)	N (SOP16)	W (SOW16)	Unit
R _{θJA} Junction-to-ambient thermal resistance	139	109	82	°C/W

13. ESD Ratings and V_{ISO}

Parameter		Test Conditions		Value			Unit
				B	N	W	
V _(ESD)	Electrostatic discharge	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001	Bus pins	±8000	±8000	±8000	V
			All pins	±4000	±4000	±4000	
		Charged-device model (CDM), per JEDEC specification		±2000	±2000	±2000	
		Machine Model JEDEC JESD22-A115-A, all pins		±200	±200	±200	
V _{ISO}	Withstand isolation voltage	V _{TEST} = V _{ISO} = 2500 V _{RMS} , t = 60 s (qualification); V _{TEST} = 1.2 × V _{ISO} = 3000 V _{RMS} , t = 1s (100% production)		2500	3750	5000	V _{RMS}

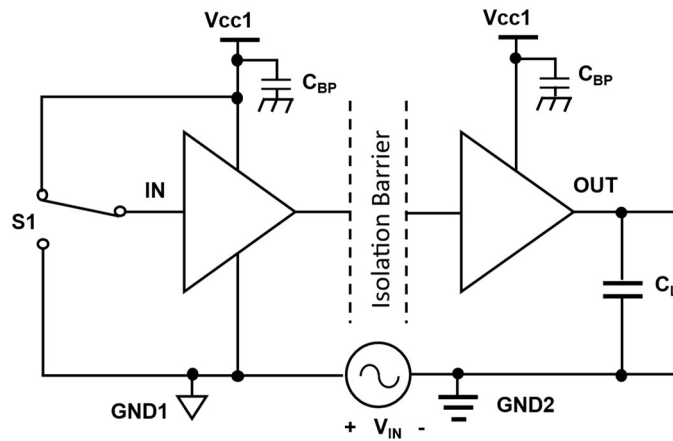
14. Parameter Measurement Information



Switching Characteristics Test Circuit and Voltage Waveforms

Notes:

1. The input pulse is supplied by a generator having the following characteristics: PRR ≤ 50 kHz, 50% duty cycle, t_r ≤ 3 ns, t_f ≤ 3 ns, Z_o = 50 Ω. At the input, 50 Ω resistor is required to terminate Input Generator signal. It is not needed in actual application.
2. C_L = 15 pF and includes instrumentation and fixture capacitance within ±20%.



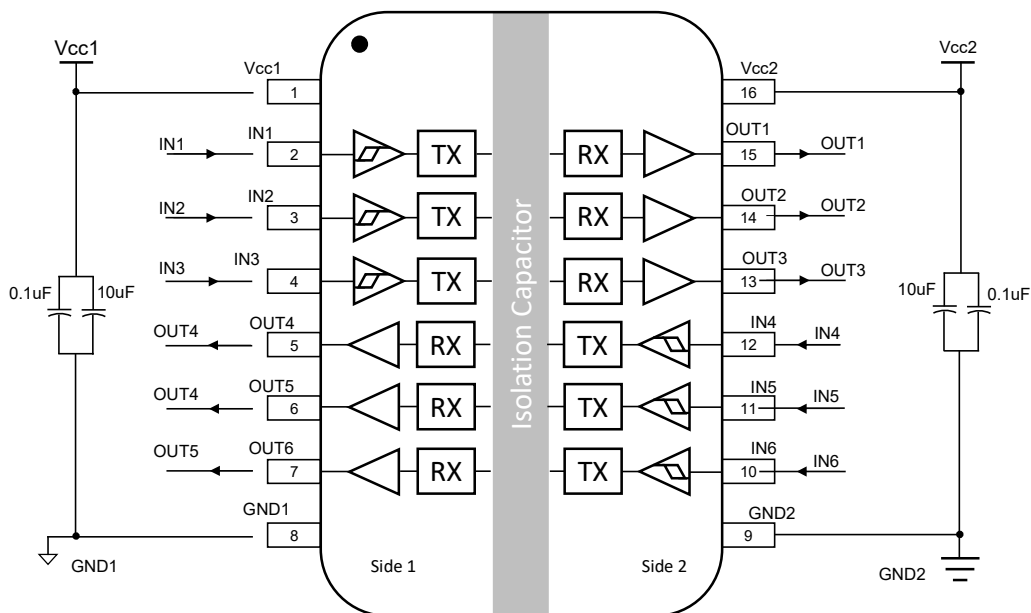
• **Common-Mode Transient Immunity Test Circuit**

Notes:

1. The High Voltage Surge Generator generates repetitive surges with $> 1\text{kV}$, $< 10\text{ns}$ rise time and fall time to reach common-mode transient noise with $> 100\text{kV}/\mu\text{s}$ slew rate.
2. $C_L = 15\text{pF}$ and includes external circuit (instrumentation and fixture etc.) capacitance.
3. Pass-fail criteria: The output must remain stable whenever the high voltage surges occur.
4. C_{BP} is bypass capacitor, $0.1\mu\text{F} \sim 1\mu\text{F}$.

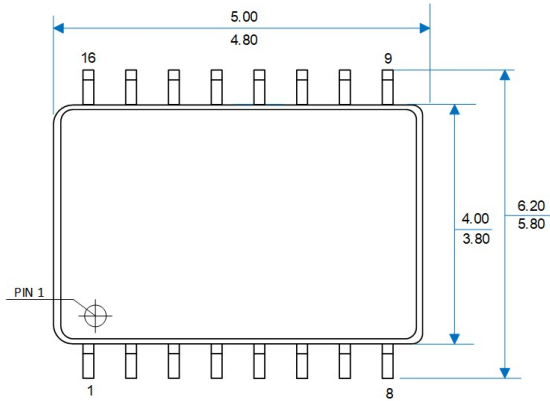
15. Typical Application

Unlike optocouplers, which require external components to improve performance, provide bias, or limit current, the SiS776x devices only require two external bypass capacitors to operate. To reduce ripple and the chance of introducing data errors, bypass Vcc1 and Vcc2 pins with $0.1\mu\text{F}$ low-ESR ceramic capacitors and parallel and $10\mu\text{F}$ capacitor to GND1 and GND2, respectively. Place the bypass capacitors as close to the power supply input pins as possible.

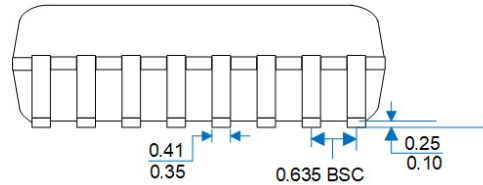


• **Typical Application Circuit of SiS776x**

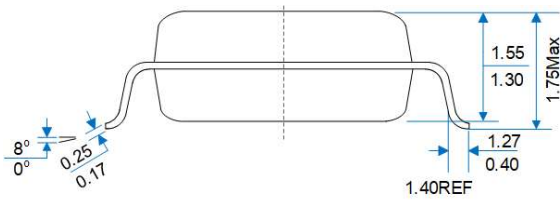
16. Package Outlines: SSOP16 (Narrow Body)



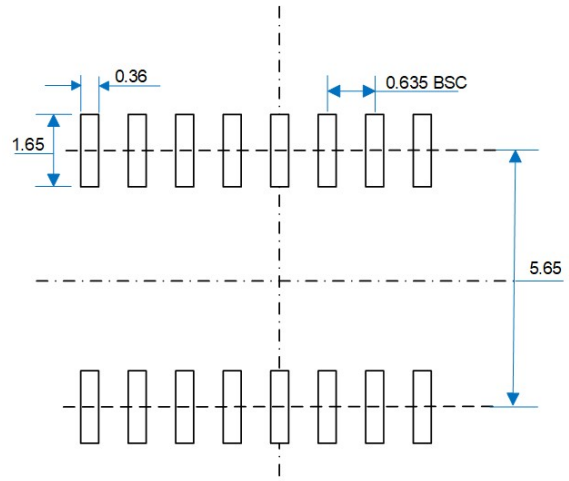
Top View



Side View 1

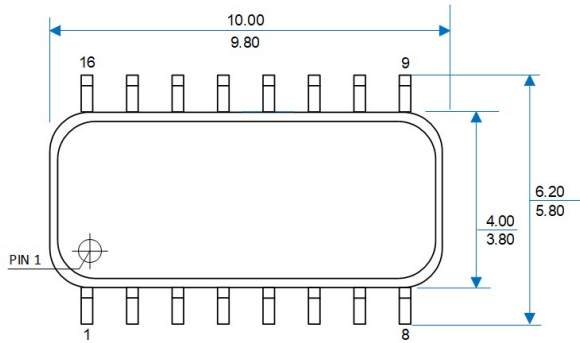


Side View 2

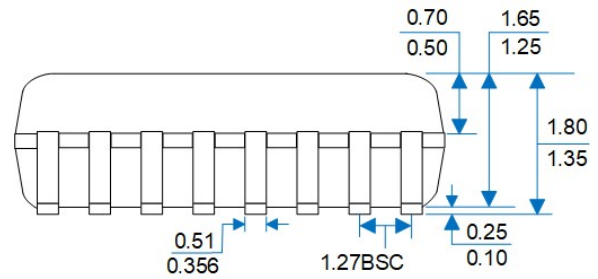


Recommended Land Pattern

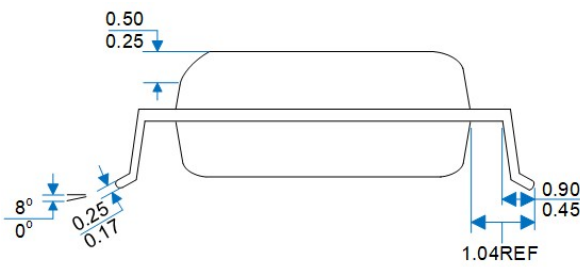
17. Package Outlines: SOP16 (Narrow Body)



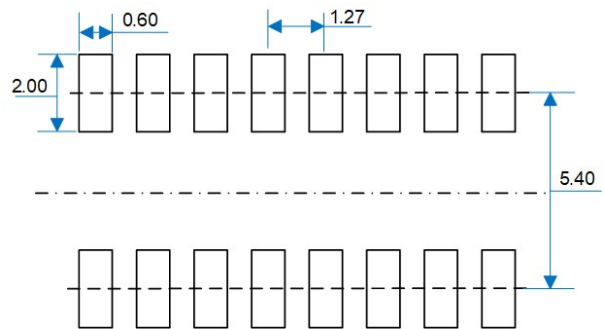
Top View



Side View 1

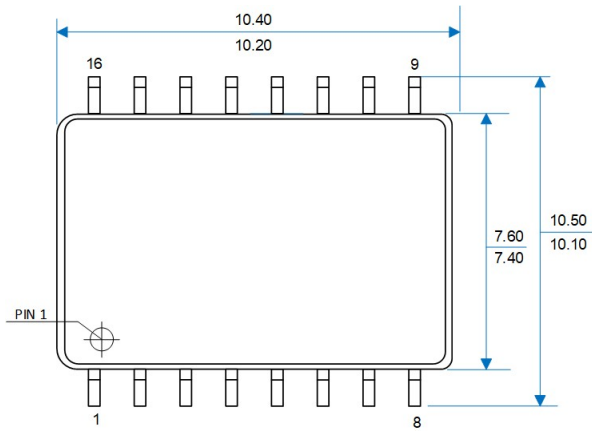


Side View 2

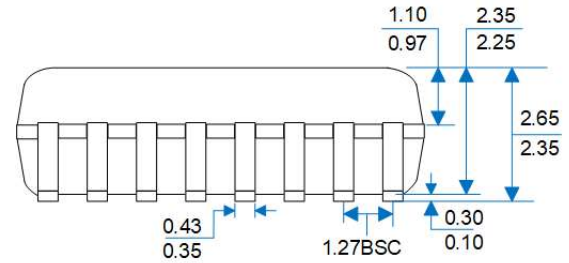


Recommended Land Pattern

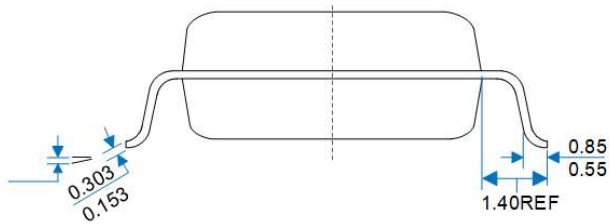
18. Package Outlines: SOW16 (Wide Body)



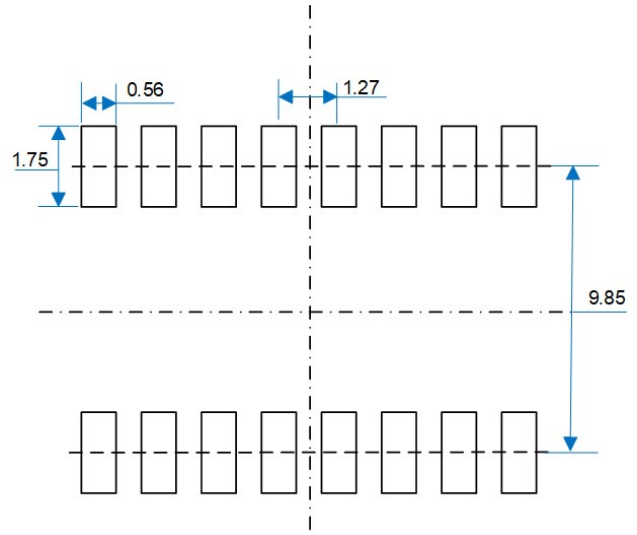
Top View



Side View 1



Side View 2



Recommended Land Pattern

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